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Each book has its strength, and Plummer's book stands out in terms of its broad, in-depth coverage of modern silicon CMOS technology. It doesn't cover MEMS or other exotic nanofabrication. Many students who've read the book said that Chap 2, where a modern CMOS process flow is described step by

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step from the substrate to back-end, was the best ...

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The SiO₂ is etched and an arsenic implant forms the N⁺ regions.

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N⁺ P The N⁺ regions are driven-in to their final junction depth.

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Problems 1.1. Plot the NRTS roadmap data from Table 1.1

(feature size vs. time) on an expanded

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EE/MSE 528 Physics and Modeling of VLSI Fabrication

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S G D S G D N P N N P N P WELL P+

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